

**CADS
2017**

Technical Program

Thursday, December 21, 2017

7:45~8:30	Welcome Reception
8:30~9:00	Opening Remarks
9:00~10:20	Keynote Speech Professor Soheil Ghiasi, Associate Professor, UC Davis Deep Convolutional Neural Network in Resource-Constrained Embedded Systems: How to Fit an Elephant in a Car?
10:20~11	Poster Session 1 & Break
11:00~13:00	Session1: Embedded Systems 1- M. Tahmasbi, H. Tabatabaee and F. Eltejaei, <i>"Delaunay based two-phase algorithm for connected cover in WSNs"</i> 2- S. Sinaei and O. Fatemi, <i>"Tree-based Design Space Exploration for Mapping Application onto Heterogeneous MPSoCs"</i> 3- P. Esfahani and H. Tabatabaee Malazi, <i>"PAMS: A new position-aware multi-sensor dataset for human activity recognition using smartphone"</i> 4- G. Korkian, N. Rahmanikia, H. Noori and J. A. Clemente, <i>"Exploration of ring oscillator based temperature sensors network accuracy on FPGA"</i>
13:00~14:30	Lunch & Break
14:30~16:00	Session2: Networks On Chips 1- P. Lotfi-Kamran, M. Modarressi and H. Sarbazi-Azad, <i>"NOC Characteristics of Cloud Applications"</i> 2- R. Akbar and F. Safaei, <i>"A Novel Congestion-aware and Adaptive Routing Algorithm in Mesh-based Networks-on-Chip with Segmentation"</i> 3- M. Ebrahimi, A. Y. Weldezion and M. Daneshtalab, <i>"NoD: Network-on-Die as a Standalone NoC for Heterogeneous Many-core Systems in 2.5D ICs"</i>
16:15~18:15	Session3: Reliable/Secure Designs 1- S. Timarchi, M. Abbasi Alaie and H. Kooshkbaghi, <i>"Novel Algorithm and Architectures for High-Speed Low-power Context-based Steganography"</i> 2- G. Ghasemi, A. M. Hosseini and H. Farbeh, <i>"RI-COTS: Trading Performance for Reliability Improvements in Commercial Of The Shelf Systems"</i> 3- M. Mushir Ahmed, D. Hely, N. Barbot, R. Siragusa, E. Perret, M. Bernier and F. Garet, <i>"Towards a robust and efficient EM based authentication of FPGA against counterfeiting and recycling"</i> 4- P. Forouhar and F. Safaei, <i>"DA-FTL: Dynamic Associative Flash Translation Layer"</i>

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Friday, December 22, 2017

8:30~9:10

Keynote Speech

Professor Kiarsh Bazargan, Associate Professor, University of Minnesota

Stochastic Computing: A Brief History

9:10~10:00

Break

11:00~12:30

Session 4: Emerging Technologies

- 1- Z. Beiki, M. Tajobian and A. Jahanian, *"Efficient Mapping of DNA Logic Circuits on Parallelized Digital Microfluidic Architecture"*
- 2- M. Shahsavari, P. Boulet, A. Shahbahrani and S. Hamdioui, *"Impact of Increasing Number of Neurons on Performance of Neuromorphic Architecture"*
- 3- M. B. Khosroshahy, M. H. Moaiyeri and K. Navi, *"Design and Evaluation of a 5-Input Majority Gate-Based Content-Addressable Memory Cell in Quantum-Dot Cellular Automata"*

12:30~13:00

Lunch & Break

13:00~14:30

Session 5: Parallel and High Performance Computing

- 1- B. Yarahmadi and F. Khunjush, *"Bamshad: A JIT Compiler for running Java Stream APIs on heterogeneous environments"*
- 2- S. N. Basharad, S. M. Nabavinejad and M. Goudarzi, *"A Novel Key Partitioning Schema for Efficient Execution of MapReduce Applications"*
- 3- M. Kiani and A. Rajabzadeh, *"SKERD: Reuse Distance Analysis for Simultaneous Multiple GPU Kernel Executions"*
- 4- A. Majd, M. Daneshtalab, J. Plosila, G. Sahebi and E. Troubitsyna, *"NOMeS: Near-Optimal Metaheuristic Scheduling for MPSoCs"*

15:00~15:30

Best Paper Announcement & Concluding Remarks